FIG. 14 5000: DIGITAL (inputted externally supplied)
VIDEO 104 **SIGNAL** SHIFT REGISTER ld1 102 ld3 Tmry **LATCH CIRCUIT** (externally) rd1 rd2 rd3 l'dn SYNCHRONIZAING SIGNAL Tsync CONTROL Tmod **VOLTAGE MODULATING CIRCUIT CIRCUIT** signals who thed 1'd1 1'02 1'03 103 l'dn Tscan **VOLTAGE / CURRENT** 107 **CONVERTING CIRCUIT** Dy2 Dy3 Dyn Ηv HIGH-Dx1 **VOLTAGE TERMINAL** Dx2 Dx3 DISPLAY PANEL Vx2 102: SCANNING CIRCUIT



Fig. XA

Common serial-to-parallel conversion as in Suzuki, etc.

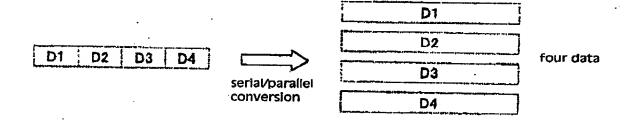


Fig. XB
Serial-to-parallel conversion of the present invention

